

RKDF University, Bhopal
Faculty Profile

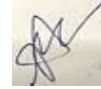


Basic Information				
Name	Dr. Ambresh Patel			
Date of Birth	15.12.1987			
Designation	Assistant Professor			
Department	Electronics and Communication Engineering			
Experience	10 Years			
Email ID	ambreshec@gmail.com			
Contact No	9584637979			
Educational Qualifications				
Description	Year	%	Institute/University	
(UG)-B.E (Electronics and Communication Engineering)	2010	55 %	Visvesvarya Technological Universit, Belgaum,Karnataka	
(PG)-M. Tech (VLSI)	2015	7.6 CGPA	Rajiv Gandhi Proudयोगiki Vishwavidyalaya, Bhopal, (M.P.)	
M. Phil.	-	-		
(Ph. D.)-(Electronics and Communication Engineering)	2024	-	RKDF University, Gandhi Nagar, Bhopal (M.P.)	
Post Doctorate	-	-	-	
NET Qualified/GATE	-	-	-	
Experience Detail				
Experience (Teaching/Research)	Designation	Duration		Name of Institute/University
		From	To	
Teaching	Assistant Professor	15.09.201 5	15.03.2019	RKDF University, Gandhi Nagar, Bhopal (M.P.)
Research	-	16.03.201 9	14.09.2023	RKDF University, Gandhi Nagar, Bhopal (M.P.)
Teaching	Assistant Professor	15.09.202 3	Till Now	RKDF University, Gandhi Nagar, Bhopal (M.P.)

Publications			
No. of Papers Published (Attach Annexure of papers published in peer-reviewed or refereed journals)	05		
No. of Books Published	Nil		
Books Chapters Published (Provide print of 1st Page of Book)	Nil		
No. of Patents Published/Grant	02		
Ph. D/M. Phil Project supervised			
Research Program	Award	Under Supervision	Name of University
Ph. D (Provide detail i.e. name, title etc.)	-	-	-
M. Phil	-	-	-
PG Thesis/Dissertation	4	4	RKDF University, Bhopal (M.P.)
Area of Expertise (100 words)			
My field is Electronics and Communication Engineering. My area of expertise consists of VLSI Design.			
Award and Achievement			
Name of Award	Description (With certified of Copy award)		
National	-		
International	-		
Conference/Seminar/Workshops/FDP			
Description	No.		
Conference/Seminar p a p e r presentation	06		
Conference/Seminar attended/ organized	03		
Work shop attended/ organized	01		
FDP Attended/ organized	03		
Research Project			
Name of Project	Funding Agencies	Amount	

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- **Any other Achievement**



Signature

1. Ambresh Patel and Dr. RiteshSadiwala, “Performance Analysis of Various CMOS Logics for High Speed VLSI Circuits”,Samriddhi –JPSET, Volume 15(1), Jan 2023. **(UGC Care Listed)**
2. Ambresh Patel and Dr. RiteshSadiwala, “Optimizing and Recuperating the Leakages in Low Voltage CMOS Circuits” Samriddhi –JPSET,Volume 14, Issue 2, 2022, pp 1-4. **(UGC Care Listed)**
3. Ambresh Patel and Dr. RiteshSadiwala “Lowvoltage cmos switch capacitor circuit” , Proceedings ofthe 7th International Conference on Electrical Energy Systems, ICEES 2021,2021,pp. 629–634, Id no.9383686**[Indexed in IEEE Xplore, SCOPUS]**
4. Ambresh Patel and Dr. Ritesh Sadiwala, “Low Voltage CMOS Switch Capacitor Circuit, 2021 7th International Conference on Electrical Energy Systems(ICEES), 11-13February2021,DOI:[10.1109/ICEES51510.2021.9383686](https://doi.org/10.1109/ICEES51510.2021.9383686)**[Indexed in IEEE Xplore, SCOPUS]**
5. AmbreshPatel,Dr.RiteshSadiwala(2021)“LowVoltageCMOSSwitchCapacitorCircuit”,IEEEExplorer,<https://www.proceedings.com/content/058/058377webtoc.pdf>**[Indexed in IEEE Explorer]**